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STATEMENT BY APPLICANT**

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Sheet 01

of 18

Application Number	10/810,410
Filing Date	March 26, 2004
First Named Inventor	Ismail Lakkis
Art Unit	2611
Examiner Name	Kevin Kim
Attorney Docket Number	00287

**U. S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
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		US- 4278978	06-14-1981	Easterling et al.	
		US- 4317210	02-23-1982	Dekker et al.	
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		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				

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		US- 5396110	03-07-1995	Houston	
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		US- 5422952	06-06-1995	Kennedy et al.	
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		US- 5633889	05-1997	Schilling et al.	
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		US- 5787118	07-28-1998	Ueda	

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		US- 5793756	08-11-1998	Ayerst et al.	
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		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				

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Sheet 08

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**Complete if Known**

Application Number	10/810,410
Filing Date	March 26, 2004
First Named Inventor	Ismail Lakkis
Art Unit	2611
Examiner Name	Kevin Kim
Attorney Docket Number	00287

**U. S. PATENT DOCUMENTS**

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		Number-Kind Code <sup>2</sup> (if known)			
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		US- 6904110	06-2005	Trans et al.	
		US- 6941378	09-2005	Apostolopoulos et al.	
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Examiner Name	Kevin Kim
Attorney Docket Number	00287

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First Named Inventor	Ismail Lakkis
Art Unit	2611
Examiner Name	Kevin Kim
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		Examiner Name	Kevin Kim
Sheet 13	of 18	Attorney Docket Number	00287

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Sheet	14	of	18	Application Number	10/810,410
				Filing Date	March 26, 2004
				First Named Inventor	Ismail Lakkis
				Art Unit	2611
				Examiner Name	Kevin Kim
				Attorney Docket Number	00287

**NON PATENT LITERATURE DOCUMENTS**

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		CELIK, A New Method to Reduce Error Propagation In Decision Feedback Equalizer, IJCI Proceedings of International Conference on Signal Processing, ISSN 1304-2386, Vol. 1, No. 2, September 2003, Pages 213-216.	
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	<b>Attorney Docket Number</b>		00287

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		FUJII et al., Implementation of Space-Time Equalizer Using Multiple Single-Constrained SMI Array Processors and MLSE, IEEE Transactions on Wireless Communications, Vol. 1, No. 2, April 2002, Pages 333-341.	
		GALLAGER, Low-Density Parity-Check Codes, Cambridge, Massachussets, July, 1963, 90 pages.	
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	Filing Date	March 26, 2004	
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		LU, et al., A Practical Implementation of Broadband Multiple Antenna Systems, 0-7803-4788-9/98 IEEE 1998, 5 pages.	
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		RYAN, An Introduction to LDPC Codes, University of Arizona, Tucson, Arizona, August 19, 2003, 23 pages.	

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